

System Memory Map for Linux+RTOS mode

Note, this file is auto generated using PyTI_PSDK_RTOS tool

Name	Start Addr	End Addr	Size	Attributes	Description
L2RAM_C7x_1_MAIN	0x7E000000	0x7E1FFFFFFF	2.00 MB	RWIX	L3 for C7x_1
L2RAM_C7x_2_MAIN	0x7E200000	0x7E3FFFFFFF	2.00 MB	RWIX	L3 for C7x_2
L2RAM_C7x_1_AUX	0x7F000000	0x7F03BFFF	240.00 KB	RWIX	L2 for C7x_1
L2RAM_C7x_1_AUX_AS_L1	0x7F03C000	0x7F03FFFF	16.00 KB	RWIX	L1 for C7x_1
L2RAM_C7x_2_AUX	0x7F800000	0x7F83BFFF	240.00 KB	RWIX	L2 for C7x_2
L2RAM_C7x_2_AUX_AS_L1	0x7F83C000	0x7F83FFFF	16.00 KB	RWIX	L1 for C7x_2
DDR_MCU1_0_IPC	0xA0000000	0xA00FFFFFFF	1024.00 KB	RWIX	DDR for MCU1_0 for Linux IPC
DDR_MCU1_0_RESOURCE_TABLE	0xA0100000	0xA01003FF	1024 B	RWIX	DDR for MCU1_0 for Linux resource table
DDR_MCU1_0_IPC_TRACE	0xA0100400	0xA01FFFFFFF	1023.00 KB	RWIX	DDR for MCU1_0 for Linux IPC trace
DDR_MCU1_0	0xA0200000	0xA0FFFFFFF	14.00 MB	RWIX	DDR for MCU1_0 for code/data
DDR_MCU2_0_IPC	0xA1000000	0xA10FFFFFFF	1024.00 KB	RWIX	DDR for MCU2_0 for Linux IPC
DDR_MCU2_0_RESOURCE_TABLE	0xA1100000	0xA11003FF	1024 B	RWIX	DDR for MCU2_0 for Linux resource table
DDR_MCU2_0_IPC_TRACE	0xA1100400	0xA11FFFFFFF	1023.00 KB	RWIX	DDR for MCU2_0 for Linux IPC trace
DDR_MCU2_0	0xA1200000	0xA2FFFFFFF	30.00 MB	RWIX	DDR for MCU2_0 for code/data
IPC_VRING_MEM	0xA5000000	0xA6FFFFFFF	32.00 MB		Memory for IPC Vring's. MUST be non-cached or cache-coherent
APP_LOG_MEM	0xA7000000	0xA703FFFF	256.00 KB		Memory for remote core logging
TIOVX_OBJ_DESC_MEM	0xA7040000	0xA8FFFFFFF	31.75 MB		Memory for TI OpenVX shared memory. MUST be non-cached or cache-coherent
APP_FILEIO_MEM	0xA9000000	0xA93FFFFF	4.00 MB		Memory for remote core file operations
TIOVX_LOG_RT_MEM	0xA9400000	0xA9FFFFFFF	12.00 MB		Memory for TI OpenVX shared memory for Run-time logging. MUST be non-cached or cache-coherent
DDR_C7x_1_IPC	0xAA000000	0xAA0FFFFFFF	1024.00 KB	RWIX	DDR for C7x_1 for Linux IPC
DDR_C7x_1_RESOURCE_TABLE	0xAA100000	0xAA1003FF	1024 B	RWIX	DDR for C7x_1 for Linux resource table
DDR_C7x_1_IPC_TRACE	0xAA100400	0xAA1FFFFFFF	1023.00 KB	RWIX	DDR for C7x_1 for Linux IPC trace
DDR_C7x_1_BOOT	0xAA200000	0xAA2003FF	1024 B	RWIX	DDR for C7x_1 for boot section
DDR_C7x_1_VECS	0xAA400000	0xAA403FFF	16.00 KB	RWIX	DDR for C7x_1 for vecs section
DDR_C7x_1_SECURE_VECS	0xAA600000	0xAA603FFF	16.00 KB	RWIX	DDR for C7x_1 for secure vecs section
DDR_C7x_1	0xAA604000	0xABFFFFFFF	25.98 MB	RWIX	DDR for C7x_1 for code/data
DDR_C7x_2_IPC	0xAC000000	0xAC0FFFFFFF	1024.00 KB	RWIX	DDR for C7x_2 for Linux IPC
DDR_C7x_2_RESOURCE_TABLE	0xAC100000	0xAC1003FF	1024 B	RWIX	DDR for C7x_2 for Linux resource table
DDR_C7x_2_IPC_TRACE	0xAC100400	0xAC1FFFFFFF	1023.00 KB	RWIX	DDR for C7x_2 for Linux IPC trace
DDR_C7x_2_BOOT	0xAC200000	0xAC2003FF	1024 B	RWIX	DDR for C7x_2 for boot section

DDR_C7x_2_VECS	0xAC400000	0xAC403FFF	16.00 KB	RWIX	DDR for C7x_2 for vecs section
DDR_C7x_2_SECURE_VECS	0xAC600000	0xAC603FFF	16.00 KB	RWIX	DDR for C7x_2 for secure vecs section
DDR_C7x_2	0xAC604000	0xADFFFFFF	25.98 MB	RWIX	DDR for C7x_2 for code/data
DDR_MCU1_0_LOCAL_HEAP	0xAE000000	0xAE7FFFFFF	8.00 MB	RWIX	DDR for MCU1_0 for local heap
DDR_MCU2_0_LOCAL_HEAP	0xAE800000	0xAEFFFFFF	8.00 MB	RWIX	DDR for MCU2_0 for local heap
DDR_C7X_1_LOCAL_HEAP_NON_CACHEABLE	0xAF000000	0xAF7FFFFFF	8.00 MB	RWIX	DDR for c7x_1 for non cacheable local heap
DDR_C7X_2_1_LOCAL_HEAP_NON_CACHEABLE	0xAF000000	0xAF7FFFFFF	8.00 MB	RWIX	Virtual address of non-cacheable DDR for c7x_1 for local heap wrt c7x_2
DDR_C7X_1_LOCAL_HEAP_NON_CACHEABLE_PHYS	0xAF000000	0xAF7FFFFFF	8.00 MB	RWIX	Physical address of non-cacheable DDR for c7x_1 for local heap
DDR_C7X_1_SCRATCH_NON_CACHEABLE	0xAF800000	0xAFFFFFFF	8.00 MB	RWIX	DDR for c7x_1 for non cacheable scratch Memory
DDR_C7X_2_1_SCRATCH_NON_CACHEABLE	0xAF800000	0xAFFFFFFF	8.00 MB	RWIX	Virtual address of non-cacheable DDR for c7x_1 for Scratch Memory wrt c7x_2
DDR_C7X_1_SCRATCH_NON_CACHEABLE_PHYS	0xAF800000	0xAFFFFFFF	8.00 MB	RWIX	Physical address of non-cacheable DDR for c7x_1 for Scratch Memory
DDR_C7X_1_LOCAL_HEAP	0xB0000000	0xB1FFFFFF	32.00 MB	RWIX	DDR for c7x_1 for local heap
DDR_C7X_2_1_LOCAL_HEAP	0xB0000000	0xB1FFFFFF	32.00 MB	RWIX	Virtual address of cacheable DDR for c7x_1 for local heap wrt c7x_2
DDR_C7X_1_LOCAL_HEAP_PHYS	0xB0000000	0xB1FFFFFF	32.00 MB	RWIX	Physical address of cacheable DDR for c7x_1 for local heap
DDR_C7X_1_SCRATCH	0xB2000000	0xB3FFFFFF	32.00 MB	RWIX	DDR for c7x_1 for Scratch Memory
DDR_C7X_2_1_SCRATCH	0xB2000000	0xB3FFFFFF	32.00 MB	RWIX	Virtual address of cacheable DDR for c7x_1 for Scratch Memory wrt c7x_2
DDR_C7X_1_SCRATCH_PHYS	0xB2000000	0xB3FFFFFF	32.00 MB	RWIX	Physical address of cacheable DDR for c7x_1 for Scratch Memory
DDR_C7X_1_2_LOCAL_HEAP_NON_CACHEABLE	0xB4000000	0xB47FFFFFF	8.00 MB	RWIX	Virtual address of non-cacheable DDR for c7x_2 for local heap wrt c7x_1
DDR_C7X_2_LOCAL_HEAP_NON_CACHEABLE	0xB4000000	0xB47FFFFFF	8.00 MB	RWIX	DDR for c7x_2 for non cacheable local heap
DDR_C7X_2_LOCAL_HEAP_NON_CACHEABLE_PHYS	0xB4000000	0xB47FFFFFF	8.00 MB	RWIX	Non-cacheable DDR for c7x_2 for local heap
DDR_C7X_1_2_SCRATCH_NON_CACHEABLE	0xB4800000	0xB4FFFFFF	8.00 MB	RWIX	Virtual address of non-cacheable DDR for c7x_2 for Scratch Memory wrt c7x_1
DDR_C7X_2_SCRATCH_NON_CACHEABLE	0xB4800000	0xB4FFFFFF	8.00 MB	RWIX	DDR for c7x_2 for non cacheable scratch Memory
DDR_C7X_2_SCRATCH_NON_CACHEABLE_PHYS	0xB4800000	0xB4FFFFFF	8.00 MB	RWIX	Physical address of Non-cacheable DDR for c7x_2 for Scratch Memory
DDR_C7X_1_2_LOCAL_HEAP	0xB5000000	0xB6FFFFFF	32.00 MB	RWIX	Virtual address of cacheable DDR for c7x_2 for local heap wrt c7x_1
DDR_C7X_2_LOCAL_HEAP	0xB5000000	0xB6FFFFFF	32.00 MB	RWIX	DDR for c7x_2 for local heap
DDR_C7X_2_LOCAL_HEAP_PHYS	0xB5000000	0xB6FFFFFF	32.00 MB	RWIX	Physical address of Cacheable DDR for c7x_2 for local heap
DDR_C7X_1_2_SCRATCH	0xB7000000	0xB8FFFFFF	32.00 MB	RWIX	Virtual address of cacheable DDR for c7x_2 for Scratch Memory wrt c7x_1
DDR_C7X_2_SCRATCH	0xB7000000	0xB8FFFFFF	32.00 MB	RWIX	DDR for c7x_2 for Scratch Memory
DDR_C7X_2_SCRATCH_PHYS	0xB7000000	0xB8FFFFFF	32.00 MB	RWIX	Physical address of cacheable DDR for c7x_2 for Scratch Memory
DDR_SHARED_MEM	0xB9000000	0xBFFFFFFF	112.00 MB		Memory for shared memory buffers in DDR
UBOOT_RELOC_MEM	0xFC000000	0xFFFFFFFF	64.00 MB		Uboot DDR relocation memory